



JOB TITLE: DESIGN VERIFICATION INTERNSHIP

LOCATION: Belgrade, Serbia

Esperanto is looking for fresh graduates or final year students of Electronics/Computer Engineering to join a growing CPU/SoC verification team working on a new line of SoC (System on Chip) devices for Machine Learning, based on RISC-V architecture.

You will join a team responsible for verifying the architecture and microarchitecture of the CPU, as well as its integration into the larger SoC.

We are looking for highly talented, dedicated, passionate, and versatile final year students or fresh graduates that are willing to get the taste of Silicon Valley in Belgrade - boost their skills, and work with SoC and CPU veterans.

What we offer

- Paid internship
- Mentoring
- Exposure to real industrial problems, from the CPU/SoC validation domain
- Exposure to the state-of-the-art tools and technologies, and ability to work with rock-stars from the industry and get the knowledge beyond the university classes and projects
- Opportunity to get the experience, and learn about modern SoC and CPU architectures
- Opportunity to get hands on real silicon
- Opportunity to get a **full-time job** after the internship, with all the benefits.

What you should bring with you

- Solid programming skills (C/Assembly)
- Understanding of Computer architecture
- Basic knowledge of SystemVerilog is a plus
- Ability to work in Linux environment is a plus
- Willingness to learn new concepts, technologies and workflows.

Interested candidates should send their CV/Resume or questions to nikola.rajovic@esperantotech.com.