



Based in Belgrade, ELSYS Eastern Europe is a subsidiary of ADVANS Group. ADVANS Group is a European group of experts in electronic systems and software. Our 1000 engineers serve a range of industries: in multimedia/wireless, service, medical, network/telecom, energy, aerospace, defense, automotive and IT.

Combining the technical expertise of numerous local entities with the strength of an international group, we propose a highly flexible offer composed of services and solutions designed to suit customer needs. ELSYS Eastern Europe is already addressing the European and American markets with the aim of developing them even more.

Junior Digital Design Verification Engineer

positions based in Belgrade

Job description:

You will be a part of a digital design verification team, constituted of verification engineers with different level of experience, from digital verification experts to digital verification juniors.

Your tasks will include:

Involvement in verification methodology establishment, verification plan creation, development of verification environment and close interaction with other IC implementation teams in order to achieve IC implementation milestones. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows. Selected candidates will receive full training and support from our experienced engineers. Project can be subject of a bachelor/master thesis or seminar paper.

Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- Skills: VHDL or Verilog, C programming language, object-oriented programming is a plus
- Strong background in digital electronics, ASIC/FPGA
- Good knowledge of English language

Good to have:

- Knowledge of UVM, Specman/e or System Verilog is a plus
- Knowledge of processor architecture and Systems On Chip is a plus
- Mobility is a plus

Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team
- Professional Development Opportunities
- Competitive Salaries & Benefits
- Compensation package includes also Additional Health Insurance, Sport & Social activities
- International Work Environment & Traveling Opportunities
- Stability

All interested candidates are invited to send **CV and motivation letter in English**, indicating the field of interest, relevant college courses and projects, on the following email: jobs@elsys-eastern.com. Deadline for application is **November 19th**. Interviews are planned for the middle/end of November and the final list of selected candidates will be announced by **November 29th**.



ELSYS Design is equal opportunity employer.

www.elsys-eastern.com