

**We are looking for you to join our international team of
successful Chip Design and Verification Engineers in our
ChipGlobe doo Belgrade Design Center**



Physical Design Engineer (f/m/d)

ChipGlobe is focusing on RTL Design– and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters is located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Center in Belgrade, Serbia and Ho Chi Minh City in Vietnam. We are more than 90 experts with an experienced management team. Based on the strong experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way. We love what we do and we do what we love.

We are offering you a very good practical and latest Industry Trend Knowledge build-up

- ✓ The position is available to fresh graduates of Electronics/Computer Engineering or junior RTL design engineers with up to 2 years of working experience
- ✓ You will start with access to cutting edge Software and EDA environment tools of big semiconductor companies and with practical training materials based on real project work. This will give you a kick start in your expertise.
- ✓ Stable and organized working environment.
- ✓ Adequate support from senior engineers of ChipGlobe, which will provide the initial training - good results and high learning ensured.
- ✓ Full time position
- ✓ Come and work with us and have a great kickstart of a successful career in Semiconductors!

Job requirements – Technical Skills

- ✓ Basic knowledge in digital electronic and RTL coding (Verilog, VHDL or SystemVerilog)
- ✓ Ideally first insights into the physical design (not required)
- ✓ Experience with EDA tools e.g. Cadence is an advantage
- ✓ Sound programming skills and knowledge in scripting languages like Tcl, Perl or Python is an advantage

Job requirements – Soft Skills

- ✓ **Communication**
 - Able to abstract technical details
 - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
 - Good level of spoken and written English – must be able to understand and create technical documentation.
- ✓ **Team player**
 - Committed to the team and task
 - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
 - Self-motivated, embracing the task
 - Highlight potential issues, organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem-solving skills**
 - Following and improving processes

Compensation

- ✓ Depends on the experience level (fresh graduate or some industrial experience)

If you are interested in this position, please send your CV and the reason why you would like to apply for it to:

employment@chipglobe.com