

NovelIC designs and provides high-tech solutions in the fields of FPGA, Embedded HW/FW and Analogue/RFIC, as well as sensor and communications algorithms. **NovelIC** develops its own patented 24GHz/60GHz/79GHz radar sensor hardware and software for various applications: human detection and tracking, SRR systems etc.

We invest in excellence, offer excellence and ask for excellence

We are pleased to enable students of the final years to be a part of our internship program in the area of:

FPGA Design

You should be:

- Final year of BSc or MSc studies student in Electronics
- A hard working student
- ◆ A positive, motivated person and a team player

You should have:

- ♦ Finished relevant courses: embedded systems, integrated computer systems, design of integrated circuits, VLSI systems etc.
- Strong theoretical background in analog and digital electronics
- ♦ The following skills are an advantage: VHDL/Verilog, Matlab/Simulink and Phyton
- Solid knowledge of English, both written and spoken

Internship program is with scholarship provided

If completion of the internship were to be successful, results can be used for writing of B.Sc. or M.Sc. thesis, subject to approval by the supervising professor at the University and Novelic. Moreover, satisfying results of the full time internship will increase the chance of full time position within the Novelic Design Centre in Belgrade to the graduated intern.

If you are interested in internship in a dynamic environment with possibility to improve knowledge in the area of analog/mixed IC design and to work with some of the greatest experts in the area send us your CV and a brief motivation letter in PDF format to the following e-mail: internship@novelic.com with following subject: NIC_Internship_FPGA_201807_YourName

The deadline for application is 31st of July 2018

For more insight check out our webpage: www.novelic.com

We are looking forward to meeting you Novelic team