

*NovelIC designs and provides high-tech solutions in the fields of FPGA, Embedded HW/FW and Analogue/RFIC, as well as sensor and communications algorithms. NovelIC develops its own patented 24GHz/60GHz/79GHz radar sensor hardware and software for various applications: human detection and tracking, SRR systems etc.*

We are continuing with various programs for students in the final years of B.Sc. and M.Sc. studies.

## **Internships**

In the area of:

## **FPGA Design**

### **Your profile:**

- Final year of Bachelor studies or Master studies student in Electronics.
- A hard-working student with good grades (GPA above 8.5).
- A positive, motivated person and a team player.
- Finished relevant university projects and courses: signal processing, programming, embedded systems.
- Strong theoretical background in analog and digital electronics.
- The following skills are advantageous: VHDL/Verilog, Matlab/Simulink, and Python
- Solid knowledge of English language, written and spoken.

**The following programs are available with scholarships provided.**

After the successful completion of the internship, work results can be used for the writing of the B.Sc. or M.Sc. thesis, subject to approval by the supervising professor at the University and NovelIC. Moreover, satisfying results of the full-time internship will increase the chances of a full-time position offering within the NovelIC Design Centre in Belgrade to the graduated intern.

Please send us your CV and a brief motivation letter by email to the following address: [internship@novelic.com](mailto:internship@novelic.com).

Your email should have the following subject: *NIC\_Internship\_FPGA\_201803\_YourName*  
Your CV and motivation letter should be in PDF format.

The deadline for applications: **31<sup>st</sup> of May 2018.**

For more insight check out our webpage [www.novelic.com](http://www.novelic.com)

*We are looking forward to meeting you!*

*Novelic Team*