

HDL Design House is one of the best companies for ASIC/System-on-Chip Design/Verification and Physical Implementation. We offer a challenging work environment, competitive salaries, additional benefits as well as opportunities for skills acquisition and development. Currently we are looking for motivated and creative individuals to join our team.

Position: **Junior/ Senior FPGA Engineer - multiple positions in Belgrade and Ćurpija**

Location: Belgrade and Ćurpija, Serbia, Europe

Education: M.Sc. Electrical Engineer or equivalent degree from Faculty of Electrical Engineering

The ideal candidate will have the following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering
- 2+ years' hands-on FPGA hardware design experience (for Seniors)
- Experience with Xilinx/Altera FPGA simulation and synthesis tools
- High speed digital design

Additional skills that would be a plus are:

- Hardware/board design
- Verilog and VHDL experience
- Embedded processors

Your key responsibilities would be:

- You will be part of the team working on the high-speed 100k+ gates FPGA design using VHDL and/or Verilog (Xilinx and/or Altera) with simulation using Cadence/Mentor simulators
- The position requires a close working with the different hardware and software engineering teams

The candidate must have the knowledge and the interpersonal skills to be able to fit into very focused group of people working on very complex product development.

All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!

We also offer:

- Comprehensive trainings for Juniors from experts for design/analog/verification of ASIC/FPGA System-on-Chip
- Unique chance to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary
- Opportunity to be part of many sports activities with colleagues
- Opportunity to travel abroad
- Engineers with no prior experience are also desirable, making this ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role

Successful training completion and testing will be condition for candidates to join larger team working in all stages of complex SoC design and verification.

For all candidates proficient knowledge of UNIX/Linux and fluent English is a prerequisite!

Please send your CV to jobs@hdl-dh.com, or fill in the application form:

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Position: **Junior/ Senior Verification Engineer—multiple positions in Belgrade and Ćurpija**

Location: Belgrade and Ćurpija, Serbia, Europe

Education: M.Sc. or equivalent degree from Faculty of Electrical Engineering

The ideal candidate will have the following skills and background:

1) Candidates without previous experience:

- M.Sc.EE or equivalent degree in Electronics, Informatics, Computer Science, Automatic, Telecommunications or Electrical engineering
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Knowledge of C/C++

2) Candidates with previous experience:

- Extensive SoC design and verification experience using VHDL, Verilog, SystemVerilog (UVM) or Specman e
- In-depth knowledge of Testbench and CPU/MCU Architecture
- Experience of Assertion based verification and Formal methods
- In-depth knowledge of ARM System Architecture
- Highly motivated and dynamic engineer

Additional skills that would be a plus are:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl).
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN)).
- Knowledge of C/C++ language and/or SystemC.
- Familiar with complex flows for System-on-Chip projects.

Your key responsibilities would be:

- involvement in all stages of complex SoC verification including specification, test-bench design, verification plan, development, verification execution and verification sign off.
- Proactive collaboration with team members in different locations
- Review of the verification environment architecture and implementation specifications.

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Position: Junior/ Senior ASIC Digital Design Engineer-multiple positions in Belgrade and Ćurpija

Location: Belgrade and Ćurpija, Serbia, Europe

Education: M.Sc. or equivalent degree from Faculty of Electrical Engineering

The ideal candidate will have the following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatic or Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly

Additional skills that would be a plus are:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl)
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN)
- Knowledge of C/C++ language

Your key responsibilities would be:

- You will be part of the team working on the high-speed multi million gates IP and SoC design using VHDL and/or Verilog
- You will be responsible for RTL development, , timing closure, synthesis, physical implementation and physical verification
- The position requires a close working with the different hardware and software engineering teams

The candidate must have the knowledge and the interpersonal skills to be able to fit into very focused group of people working on very complex product development.

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Position: **Junior/Senior Analog Engineer - multiple positions in Belgrade and Ćurpija**

Location: Belgrade and Ćurpija, Serbia, Europe

Education: M.Sc. or equivalent degree from Faculty of Electrical Engineering

The ideal candidate will have the following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering
- Highly motivated and dynamic engineer able to adopt new technology quickly

The following skills will be a plus:

- Analog design experience
- Basic experience with state of the art analog design tools
- Experience with the implementation technologies below 90nm/CMOS and all steps in analog design flow

Your key responsibilities would be:

You will be part of the team working on the implementation of advanced analog functions layout

Layout, simulations and circuit design will be part of your daily tasks

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